

FIG 1

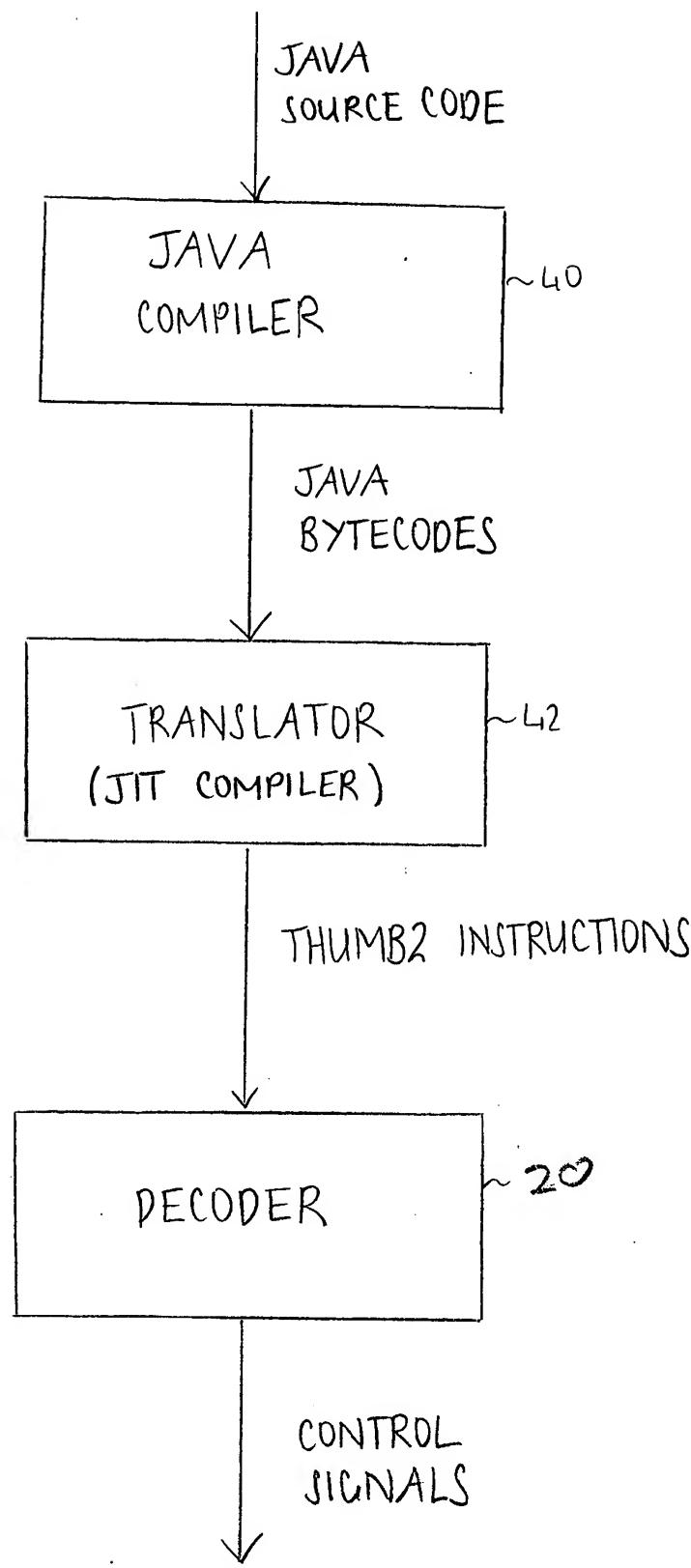


FIG 2

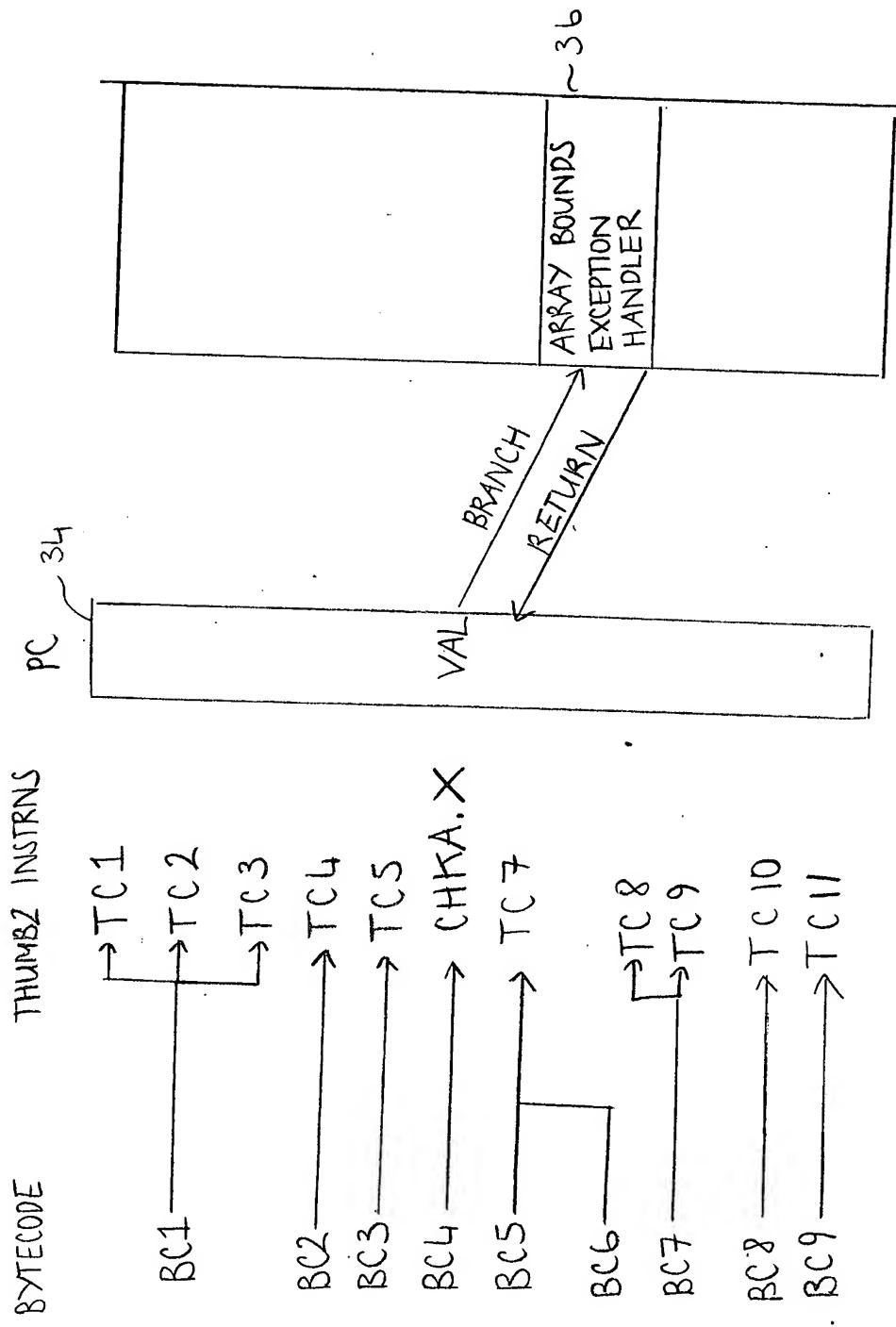


Fig. 3

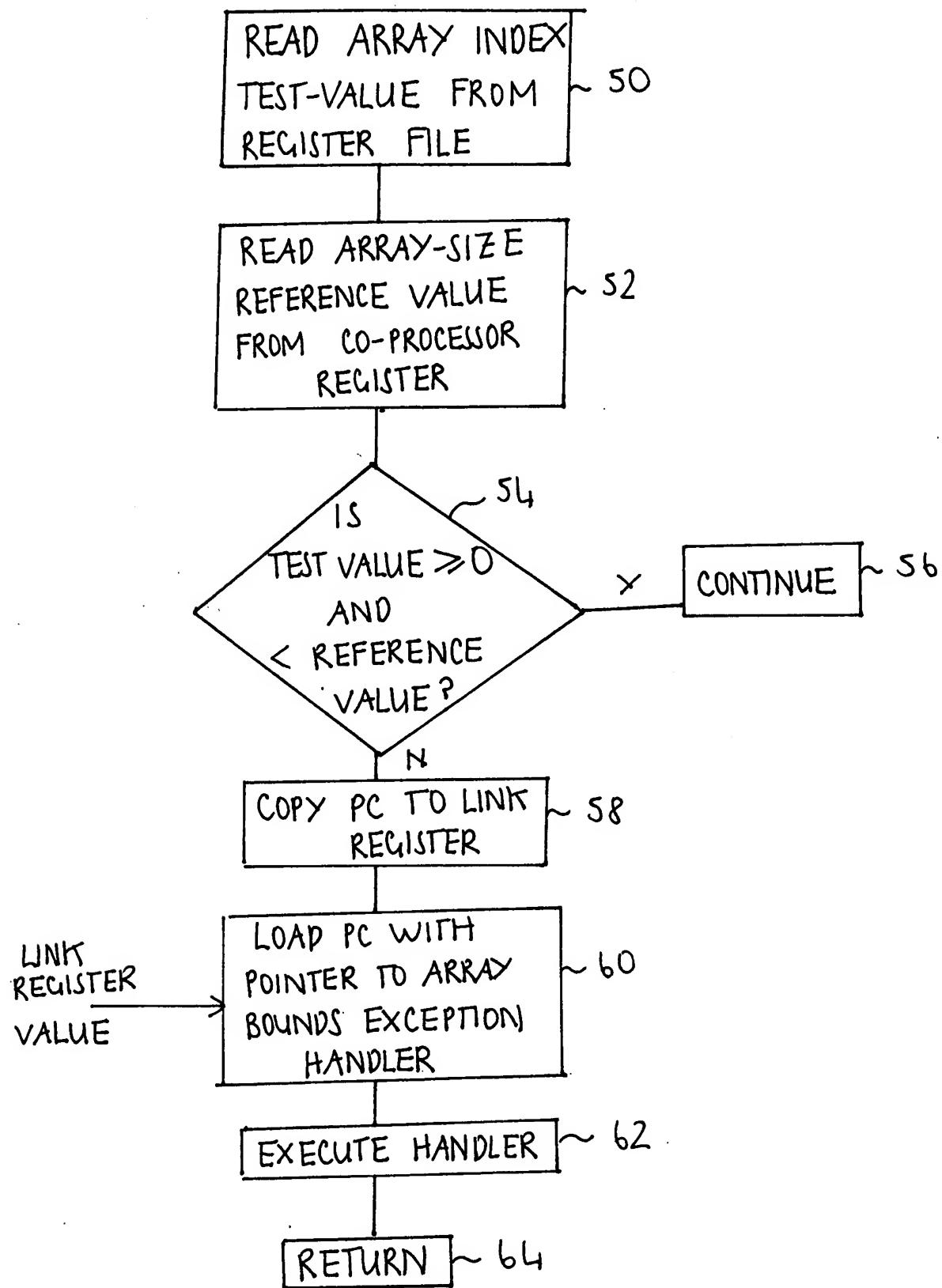


FIG 4

Instruction	CHKA .X	Rn, Rm (16-bit)		
Encoding	15 14 13 12 11 10 9 8 7 6 5 3 2 0			
	< op code >	H1 H2 Rm Rn		
Thumb-2 Equivalent	CMP Rn, Rm MOV LS lr, pc ADD LS pc, HandlerBase, #-8			
Definition	IF (unsigned) Rm >= (unsigned) Rn.  lr = pc  pc = HandlerBase, #-8 ; IndexException			
Encoding space	2 <sup>8</sup>	8 bits		
Note	This is based upon the CMP(3) 16-bit Thumb-2 instruction that can use high registers			
Note	H1 contains the most significant bit for Rn, H2 the most significant bit for Rm			
Note	The LS case should almost never occur, so can be treated as exceptional behaviour			
Note	This instruction does not set condition flags			
Note	This comparison is UNSIGNED			
Note	Return stack prediction will not be required when the MOV lr,pc step is executed.			

FIG 5